



YOUR  
TECHNOLOGY  
PARTNER



# WITH YOU FROM START TO FINISH

WE ARE A LEADER IN THE COMPUTER-AIDED ENGINEERING, DESIGN, MANUFACTURING AND ASSEMBLY OF COMPLEX PRINTED CIRCUIT BOARDS.

WORKING IN PARTNERSHIP WITH YOU, WE DEVELOP NEXT LEVEL, SIMPLE SOLUTIONS FOR THE SOPHISTICATED ATE WORLD. WE DELIVER IN THE SHORTEST TIME TO MARKET, IN ORDER TO HELP YOU SUSTAIN YOUR COMPETITIVE ADVANTAGE.

## CAPABILITIES

BOARD CONSTRUCTION	▶ PRESENT ◀ Standard Capability	▶ PRESENT ◀ Special Capability	▶ ON GOING ◀ Current Research & Development for Future Capabilities
Maximum number of layers	Up to 50	51 to 66	67 to 72
Warpage (with balanced construction)	0.4%	0.2%	< 0.2 %
Minimum dielectric thickness	0.0027"	0.0027"	0.0017"
<b>INNER LAYER</b>			
Minimum line width	0.004"	0.003"	0.0025"
Minimum space trace/trace	0.004"	0.003"	0.0025"
<b>OUTER LAYER</b>			
Minimum line width	0.003"	0.002"	0.002"
Minimum space trace/pad	0.004"	0.003"	0.0025"
Minimum space trace/trace	0.004"	0.003"	0.0025"
Aspect Ratio for hole sizes > 0.005	30:1	40:1	50:1
Minimum SMT size	0.005"	0.004"	0.003"

0.004" drilled hole in a 0.160" thick board (40:1)

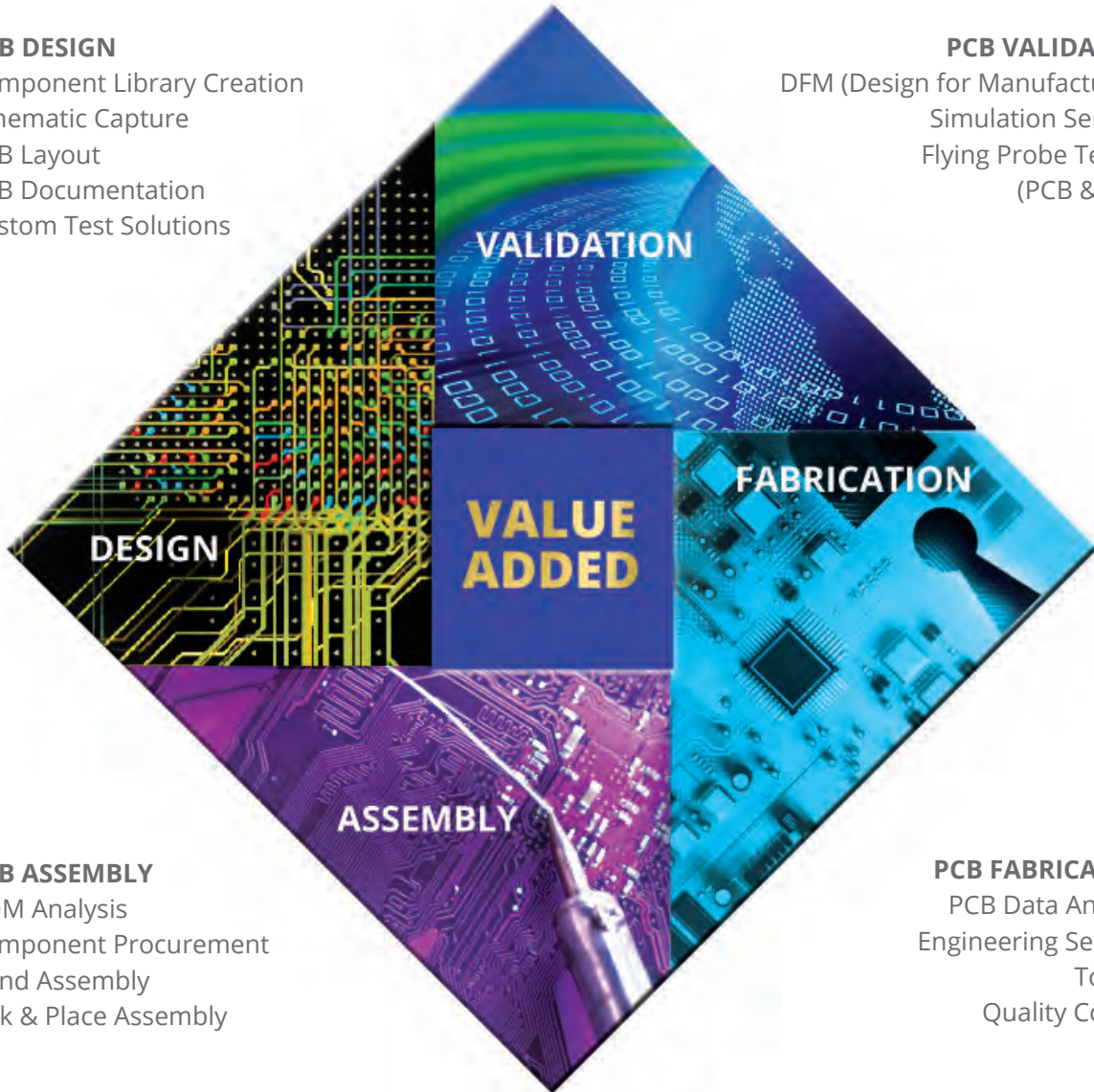
**SPECIALIZED IN SEQUENTIAL LAMINATION PROCESS with Blind & Buried vias** (down to 0.35mm pitch)

**PCB DESIGN**

Component Library Creation  
Schematic Capture  
PCB Layout  
PCB Documentation  
Custom Test Solutions

**PCB VALIDATION**

DFM (Design for Manufacturing)  
Simulation Services  
Flying Probe Testing  
(PCB & PCA)



**PCB ASSEMBLY**

BOM Analysis  
Component Procurement  
Hand Assembly  
Pick & Place Assembly

**PCB FABRICATION**

PCB Data Analysis  
Engineering Services  
Tooling  
Quality Control

SOFTWARE RESOURCES INCLUDE:



CADENCE  
DESIGN SYSTEMS  
Allegro PCB Designer  
Allegro Design  
Entry Capture,  
Capture CIS  
OrCAD Capture



MENTOR  
GRAPHICS  
Expedition PCB  
DxDesigner  
PADS Layout  
PADS Logic



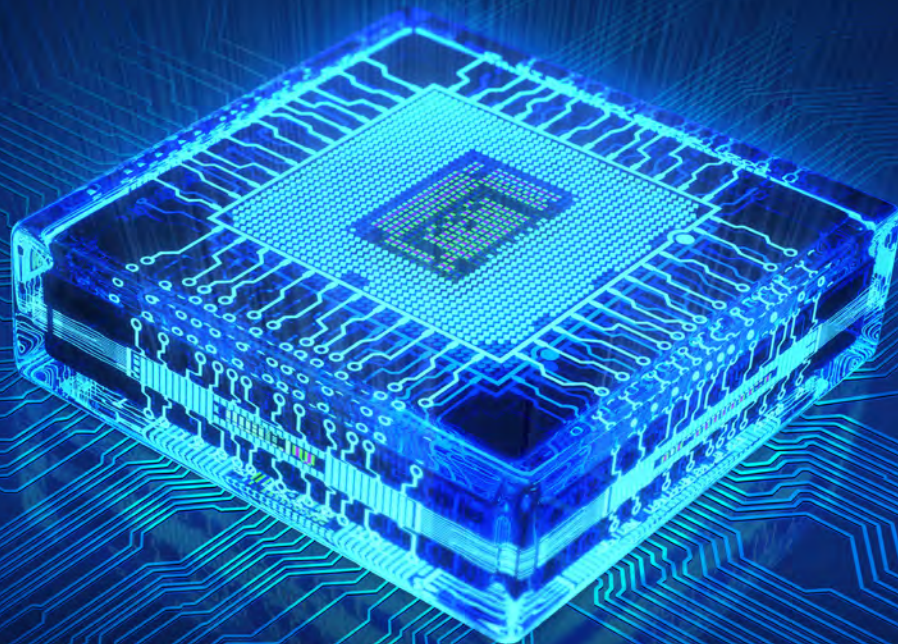
FRONTLINE  
PCB SOLUTIONS  
Genesis 2000



ALTIUM  
Altium



DOWNSTREAM  
TECHNOLOGIES  
CAM 350



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